

FIG. 2

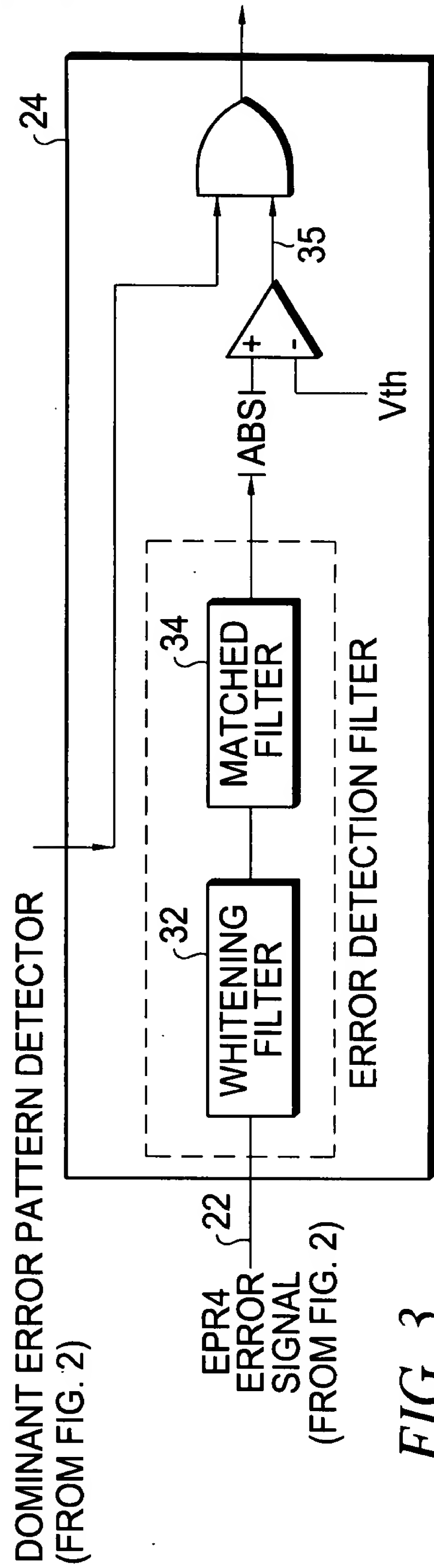


FIG. 3

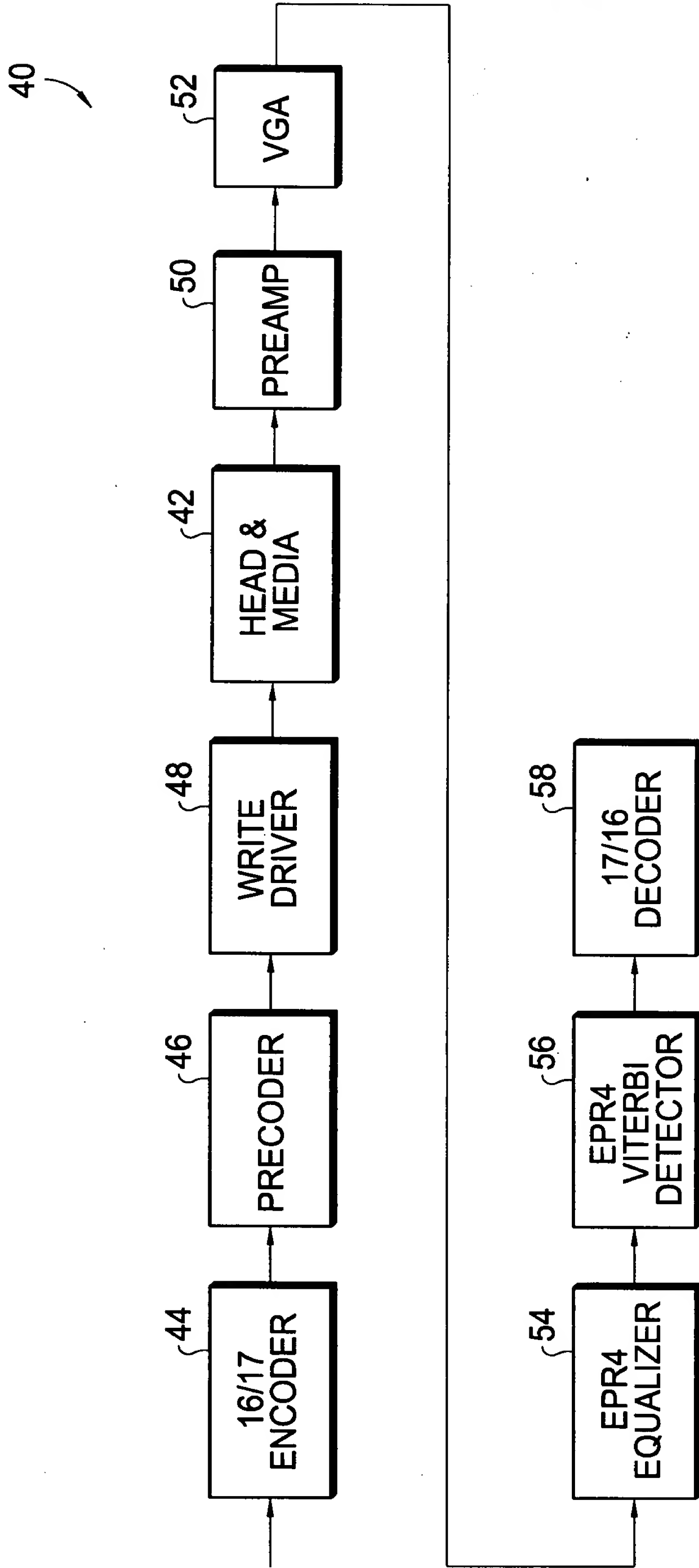
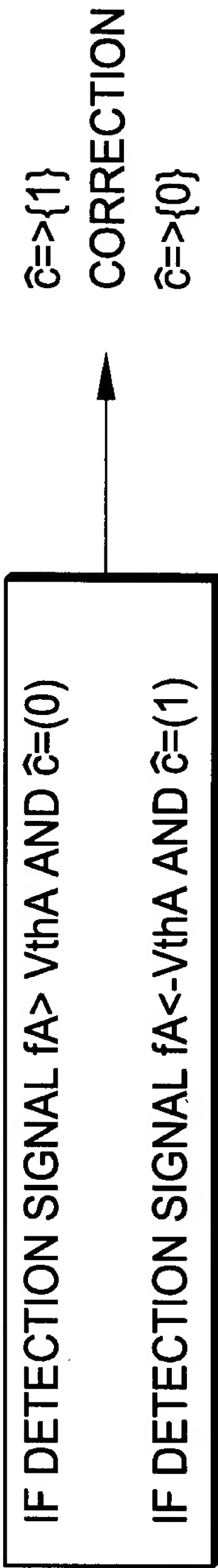


FIG. 4

ERROR EVENT A ($ex=\{1\}$ OR $ex=\{-1\}$)



ERROR EVENT B ($ex=\{1,-1,1\}$ OR $ex=\{-1,1,-1\}$)

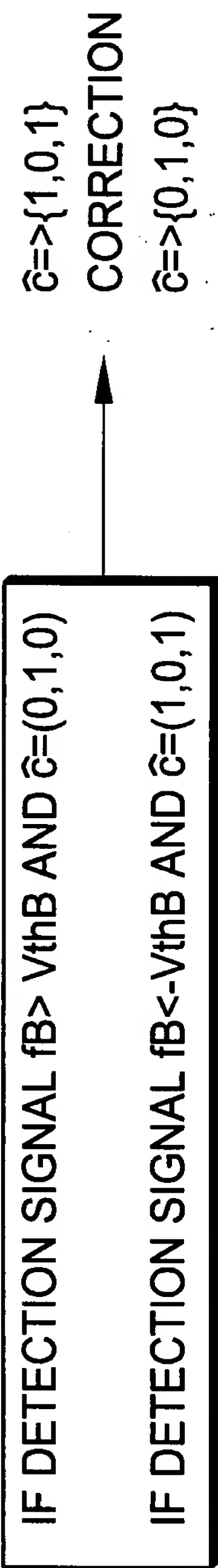


FIG. 5

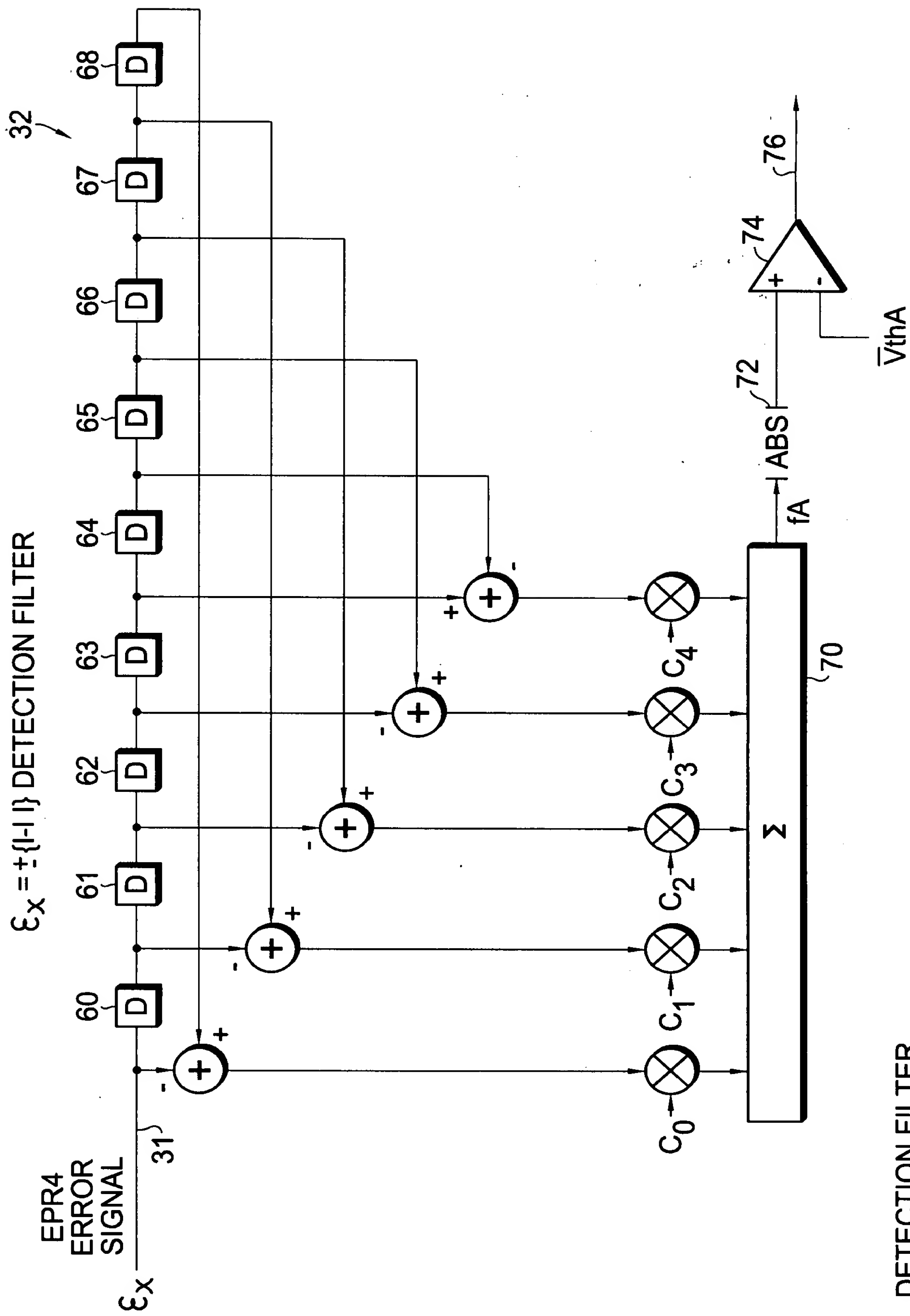


FIG. 6

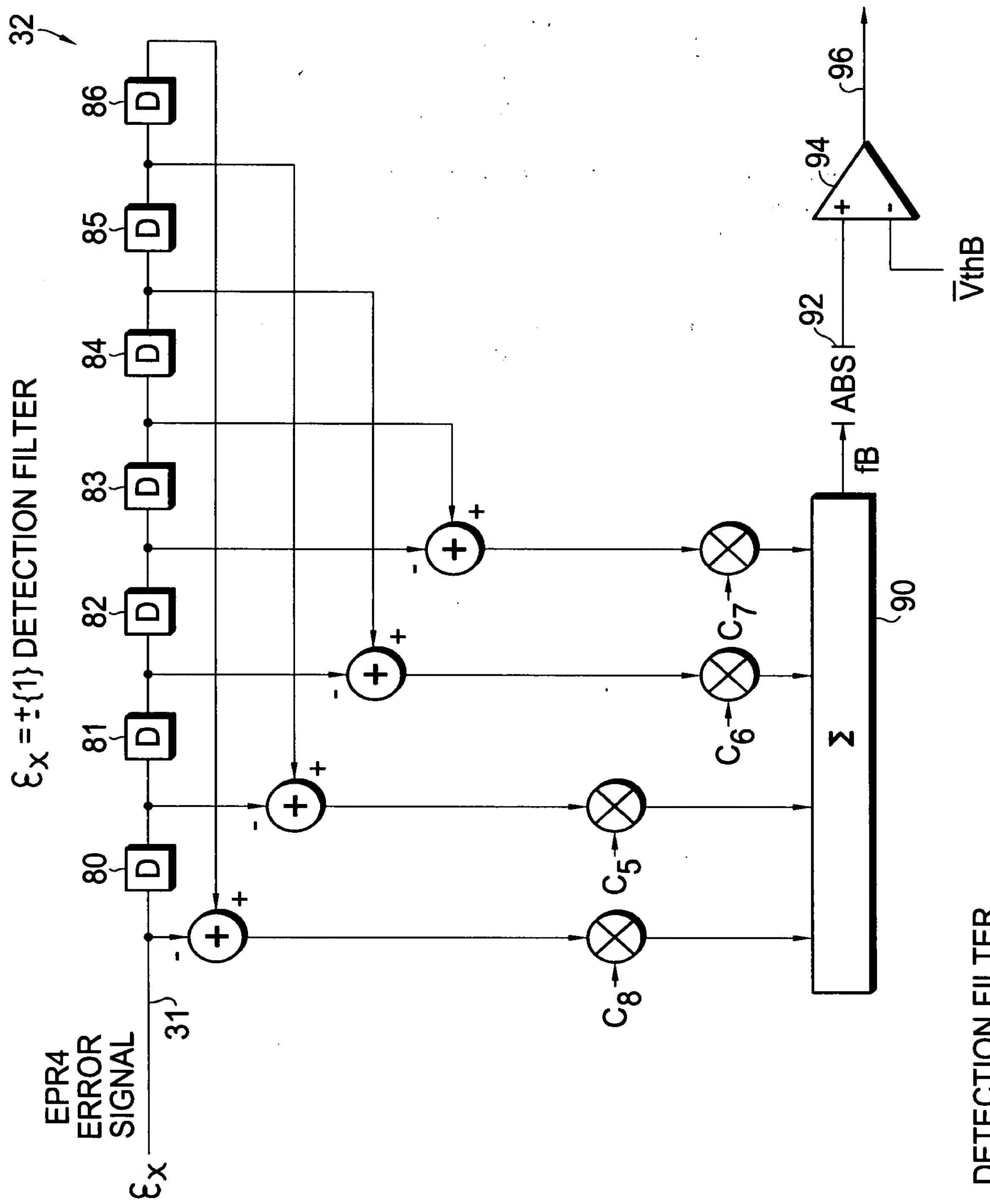
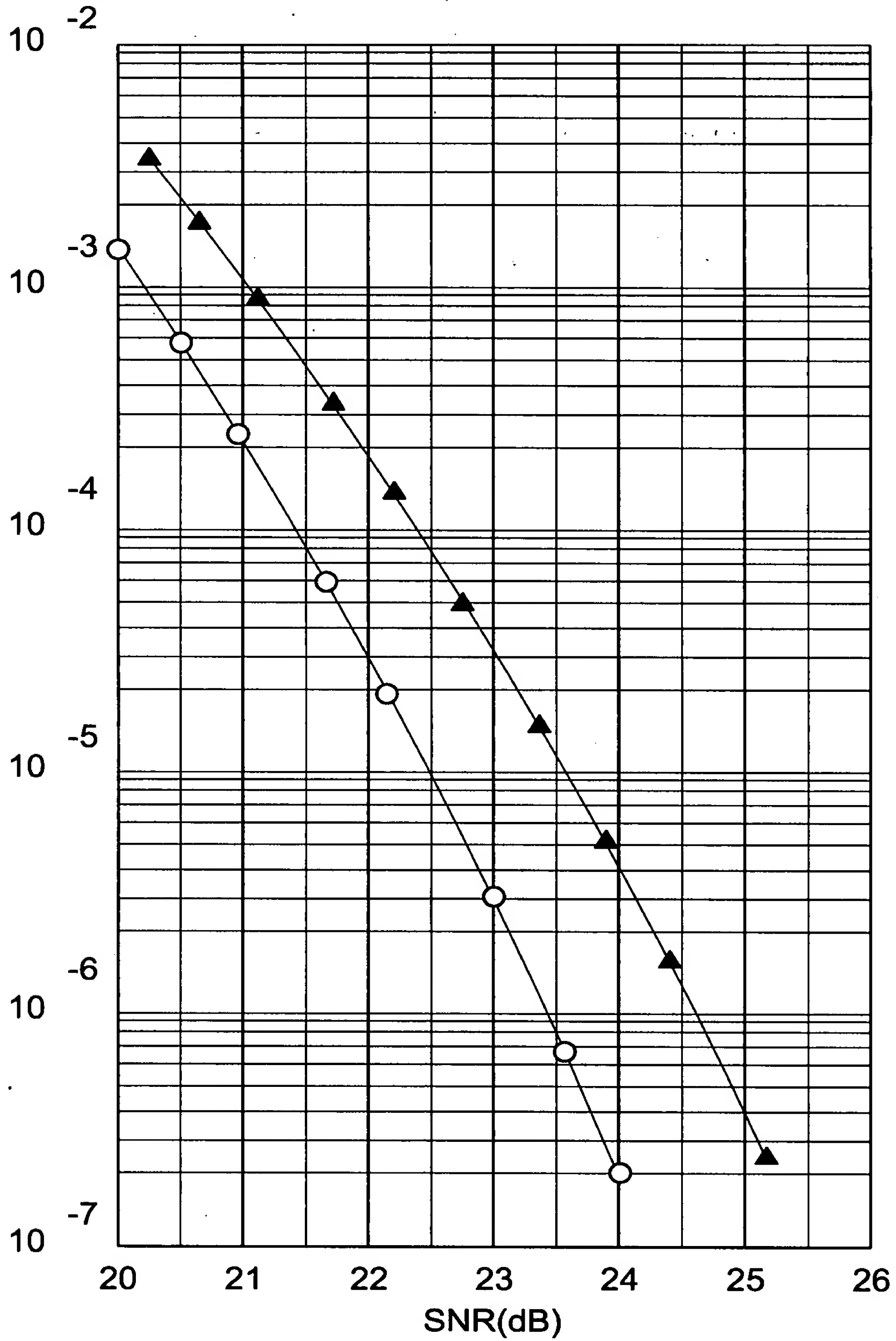


FIG. 7

BIT ERROR RATE VS SNR

BIT ERROR RATE



▲ W/O POST PROCESSOR
○ WITH POST PROCESSOR

FIG. 8

ERROR DETECTION FILTER

$e(k)$: EEPR4 ERROR SIGNAL

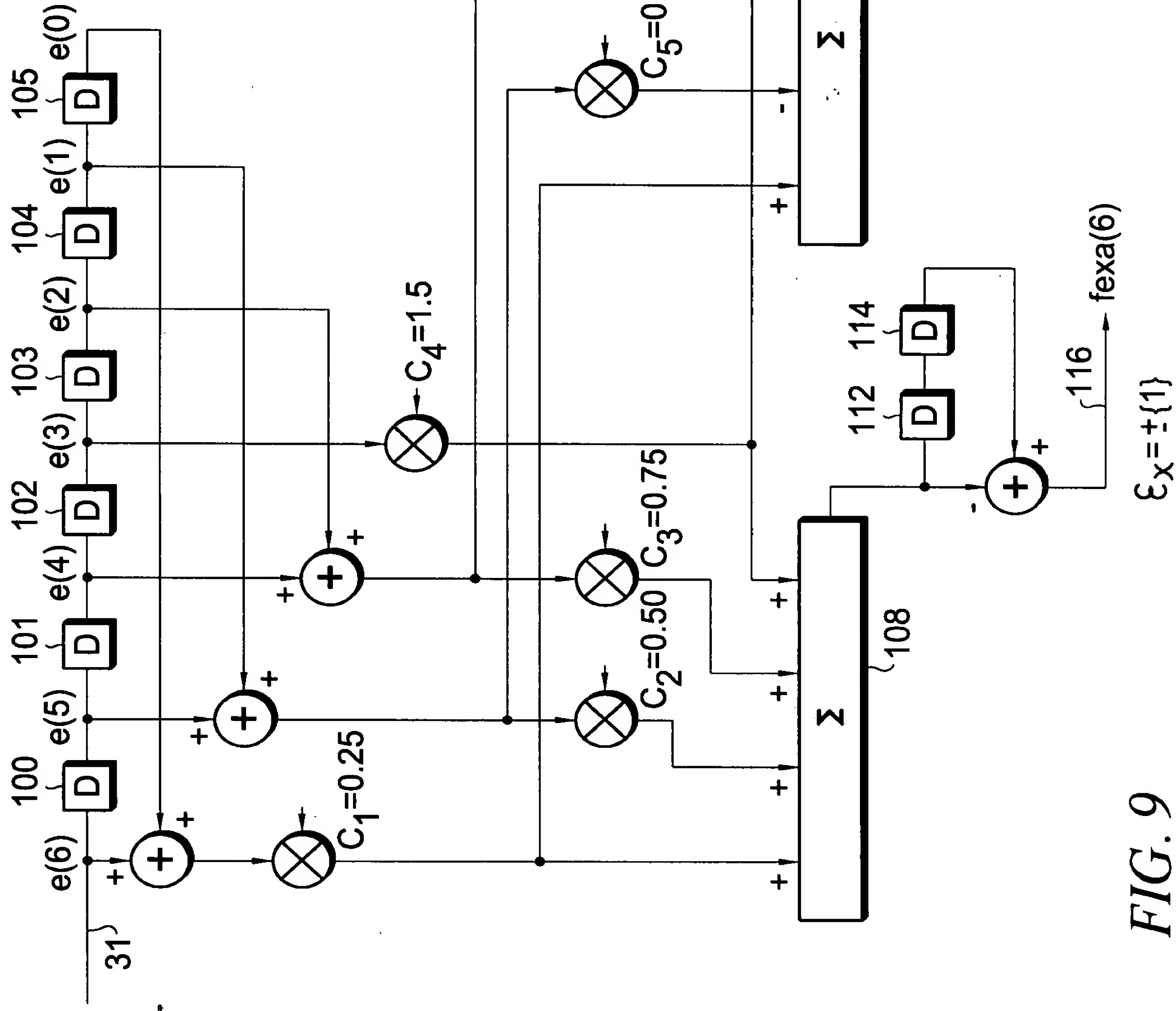


FIG. 9